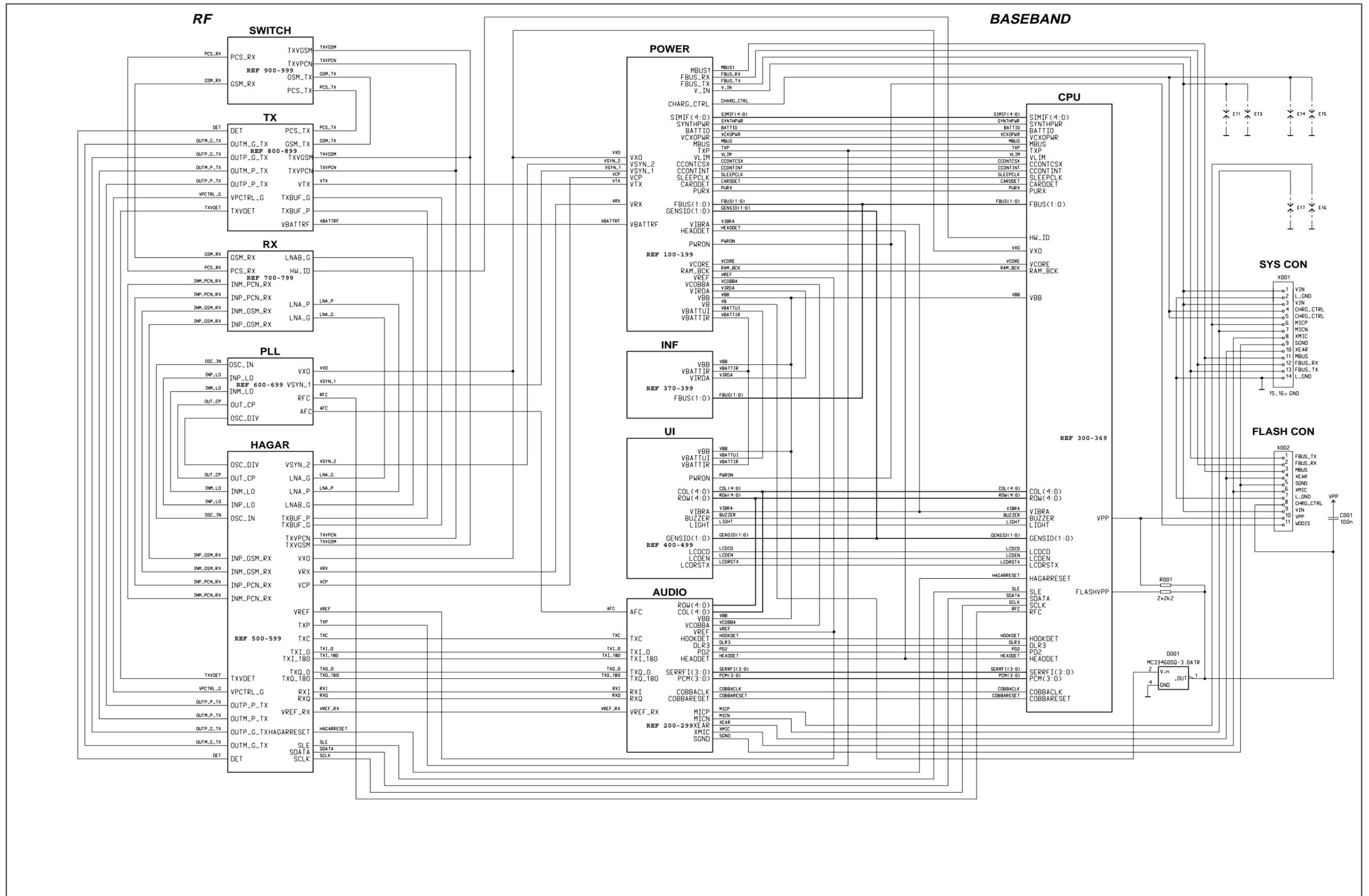
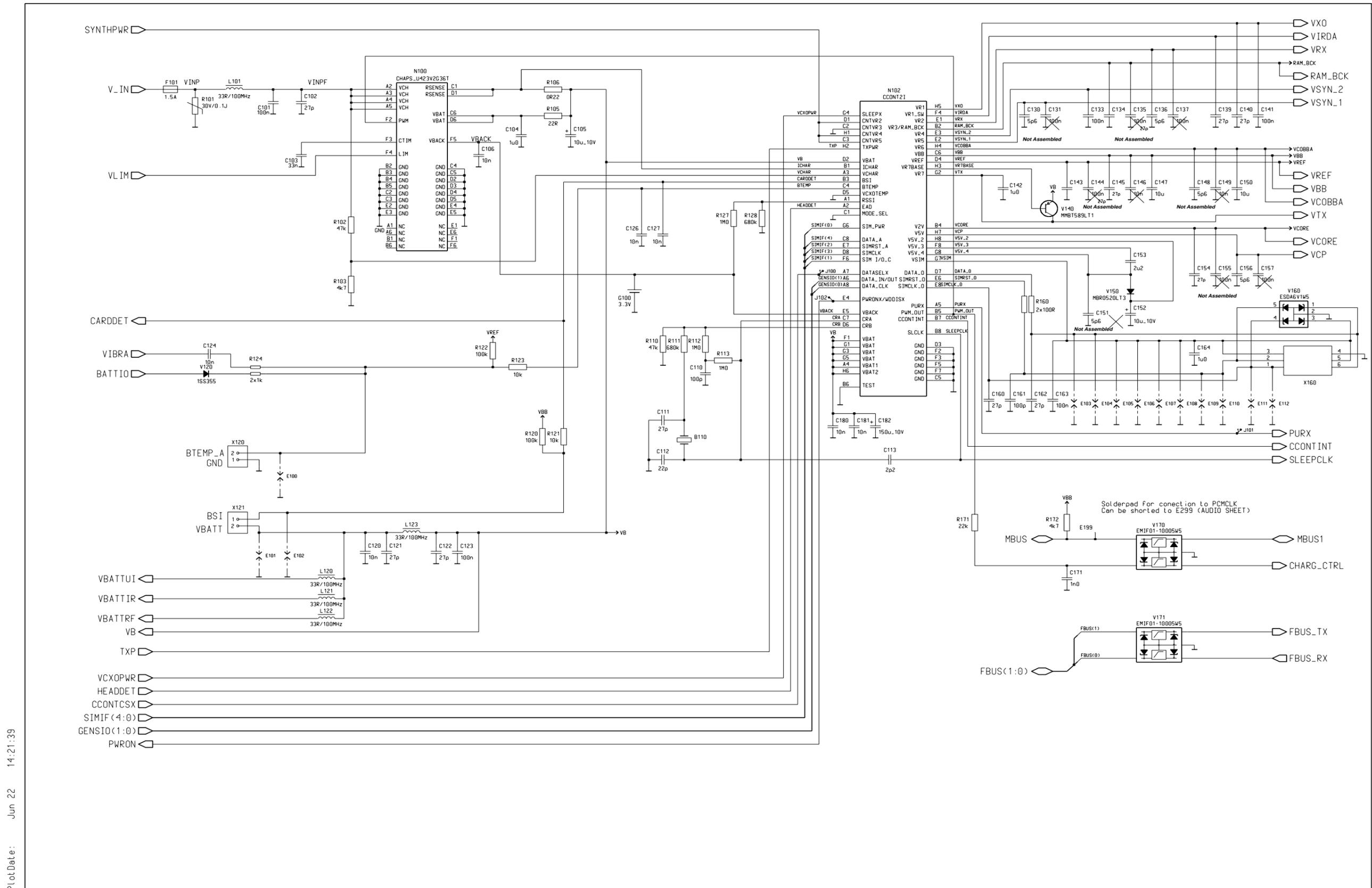


Baseband / RF Interconnections



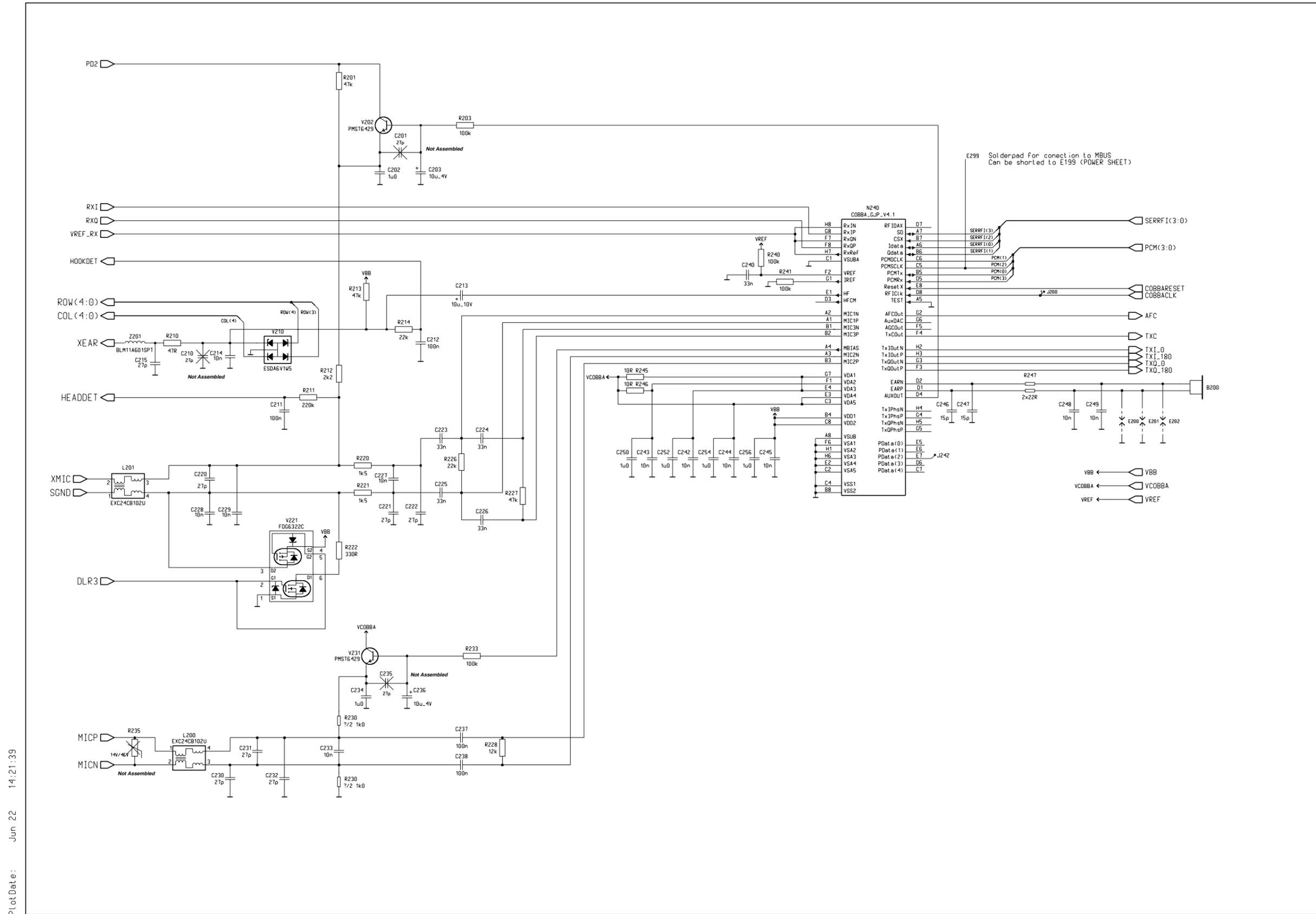
Plot Date: Jun 22 14:21:39

Power



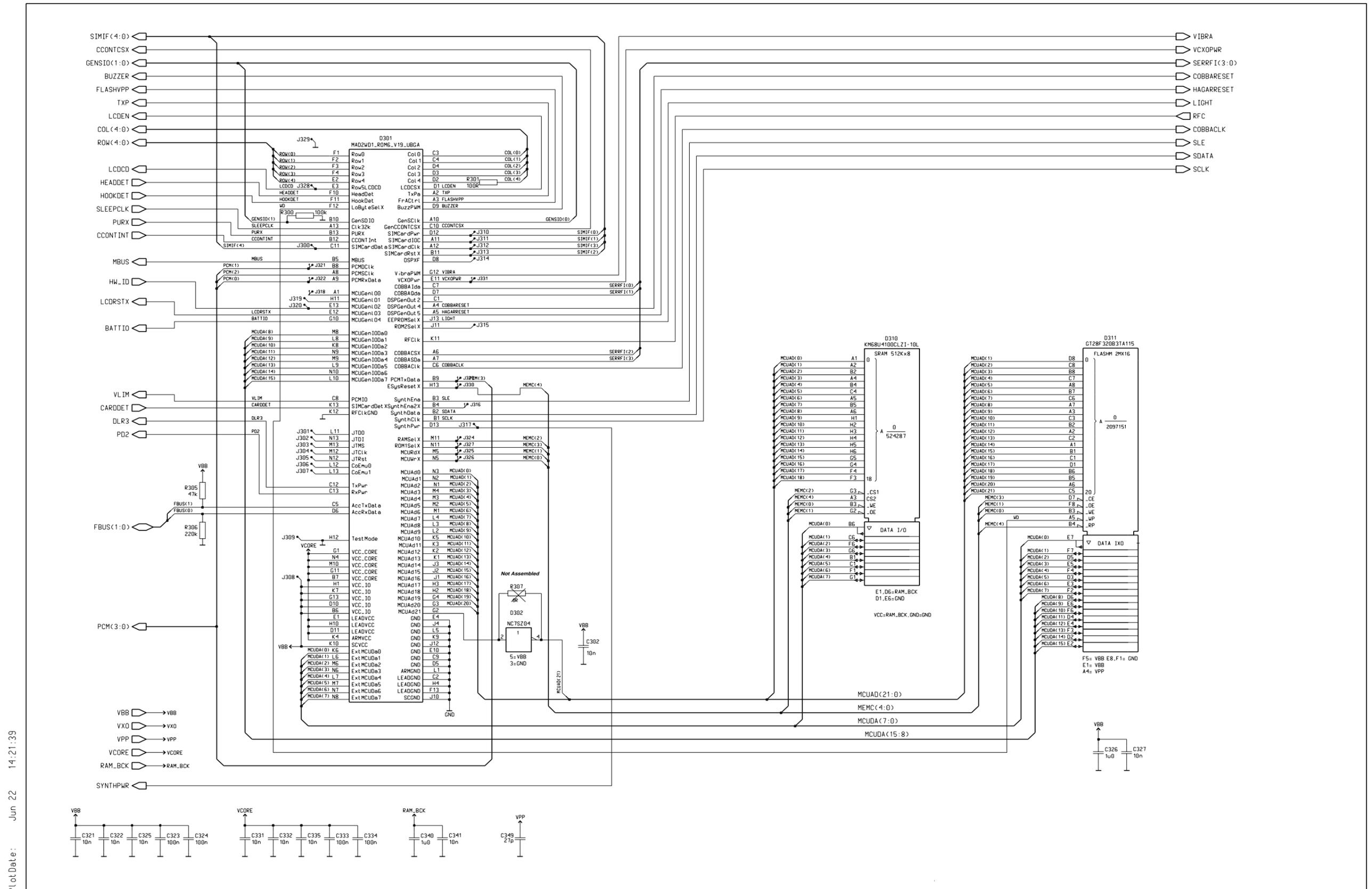
Plot Date: Jun 22 14:21:39

Audio



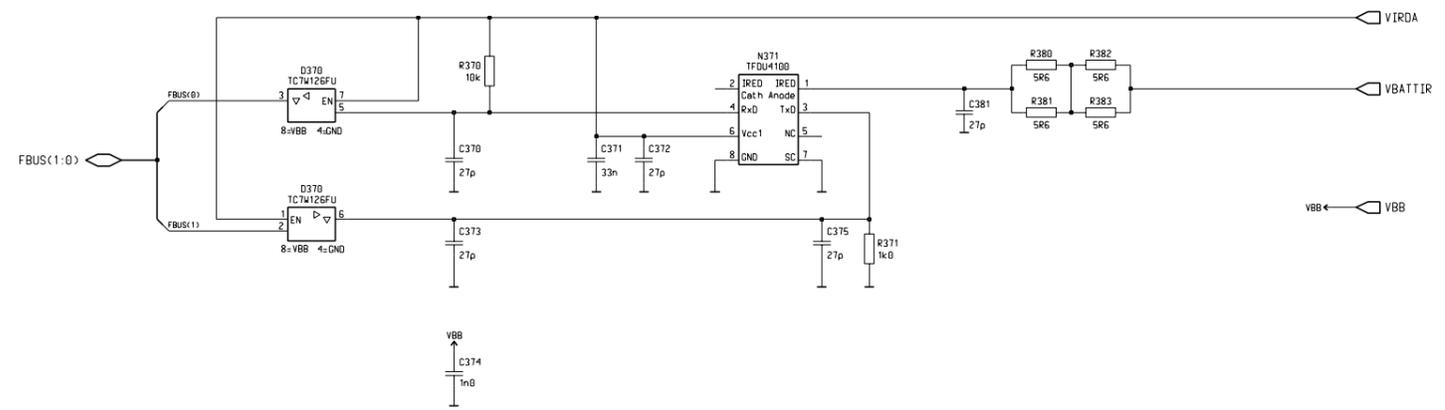
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CPU



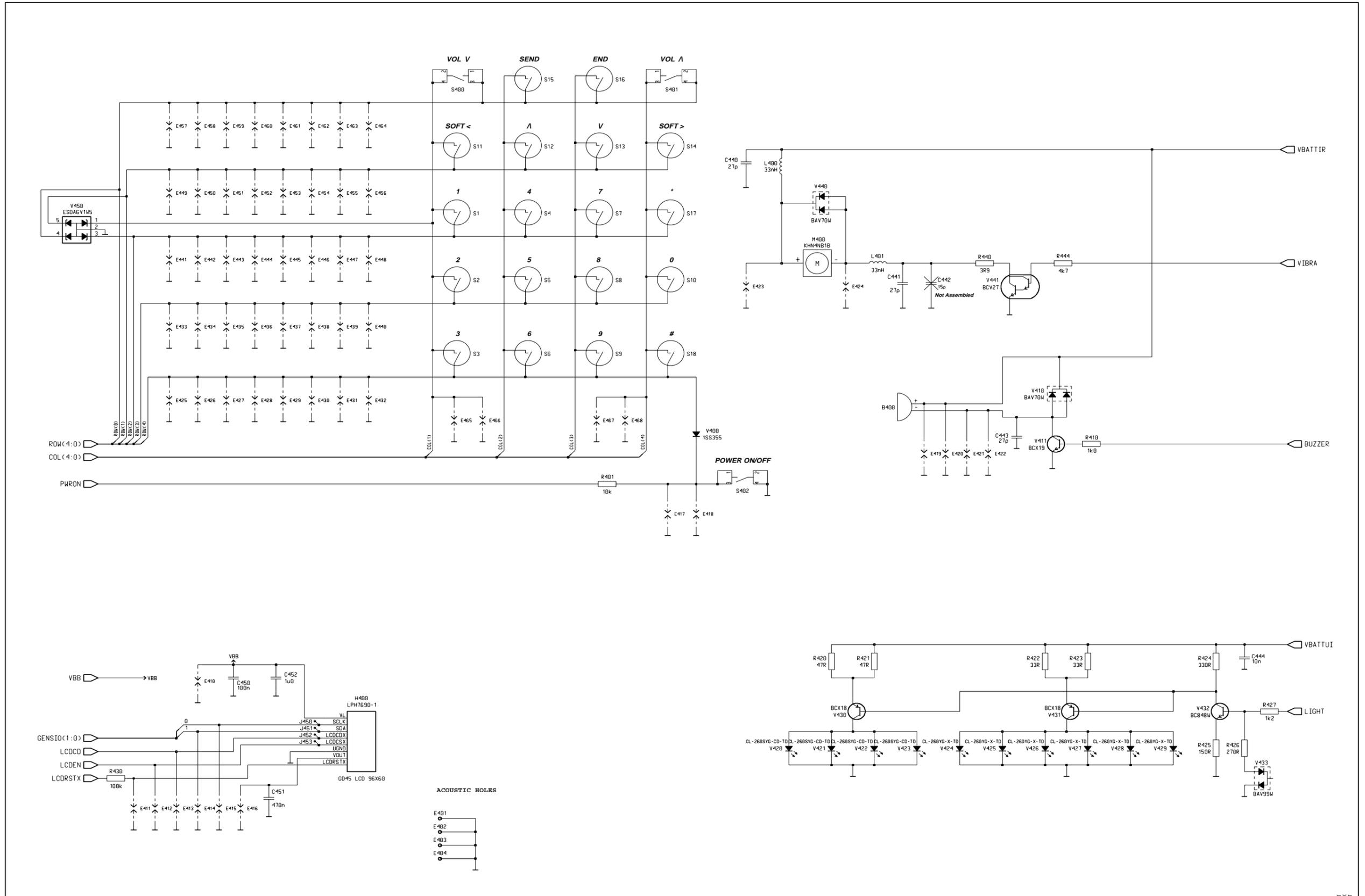
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Infra red Module



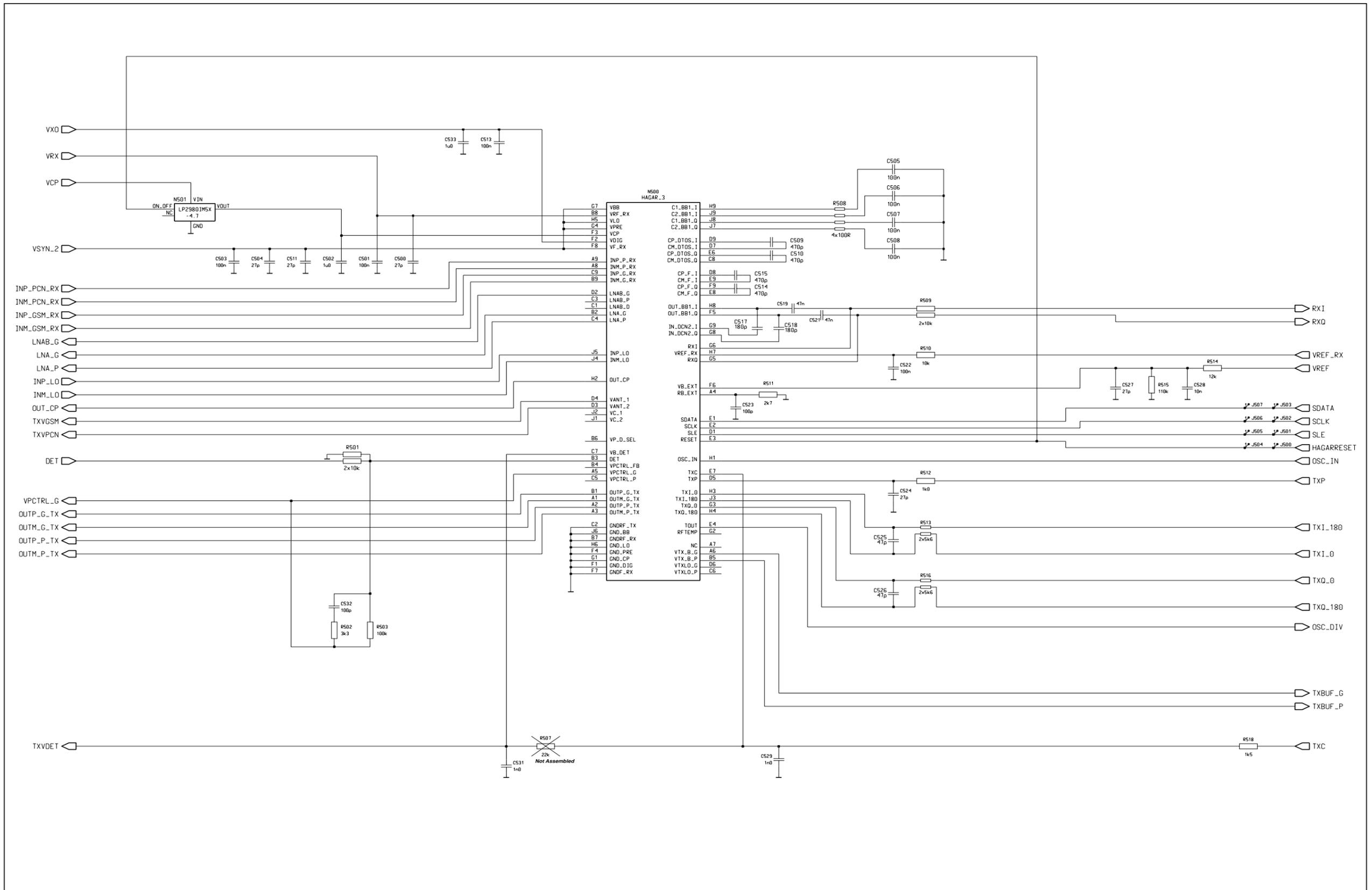
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PLOT

User Interface



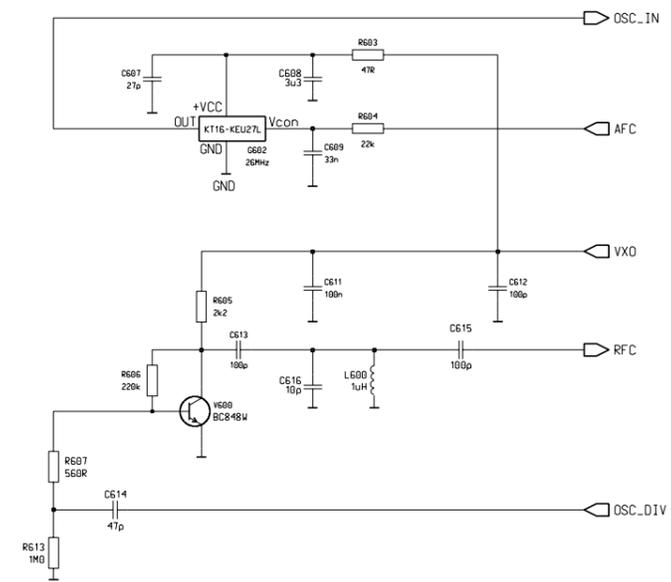
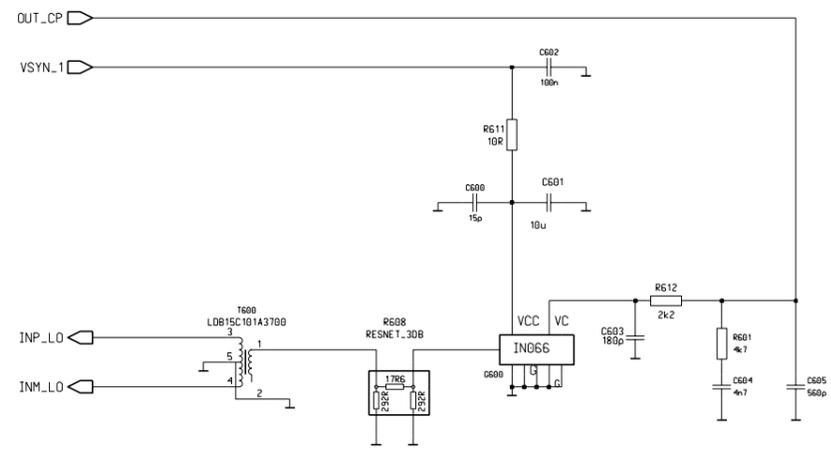
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Hagar

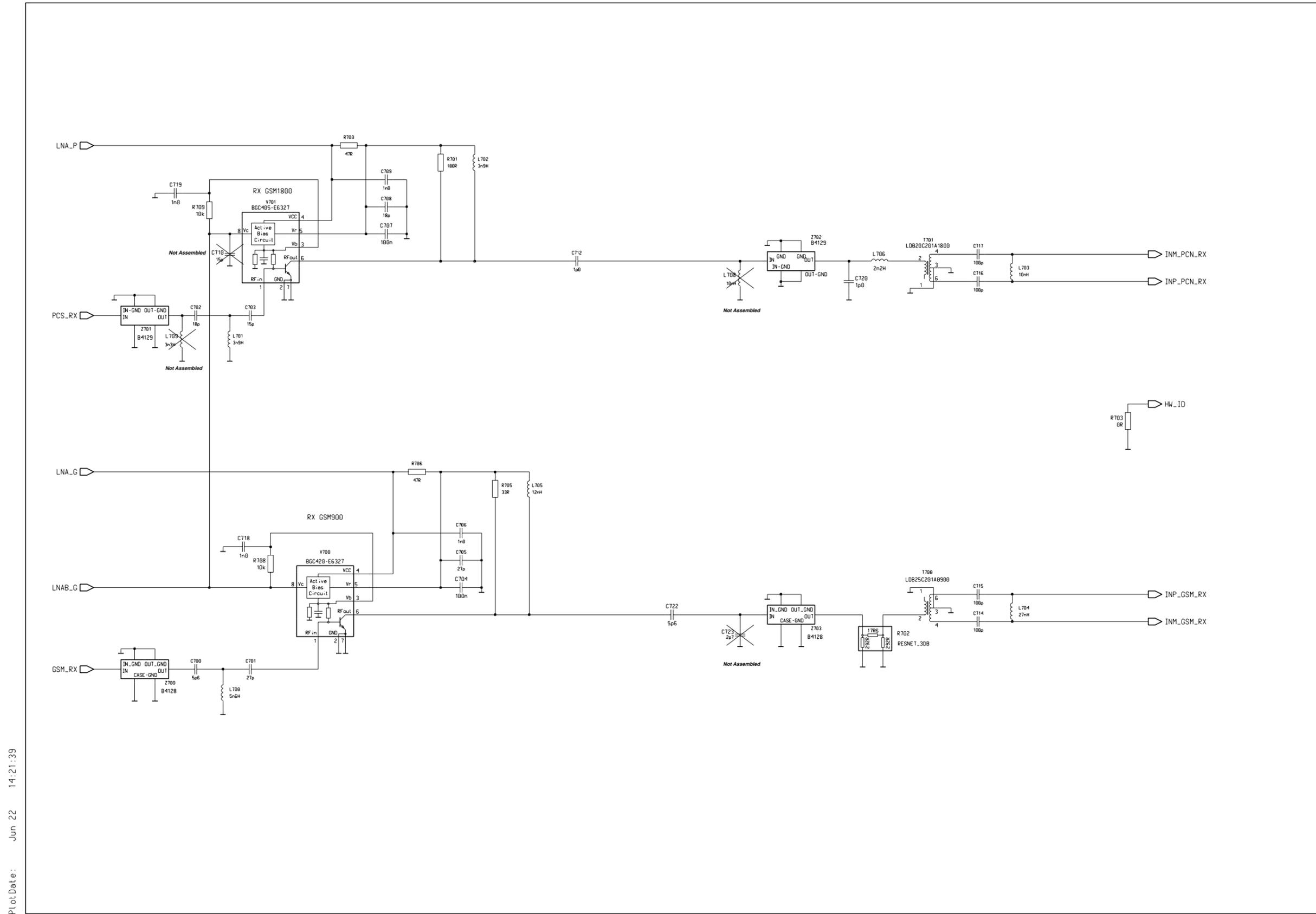


PLL

Plot Date: Jun 22 14:21:39

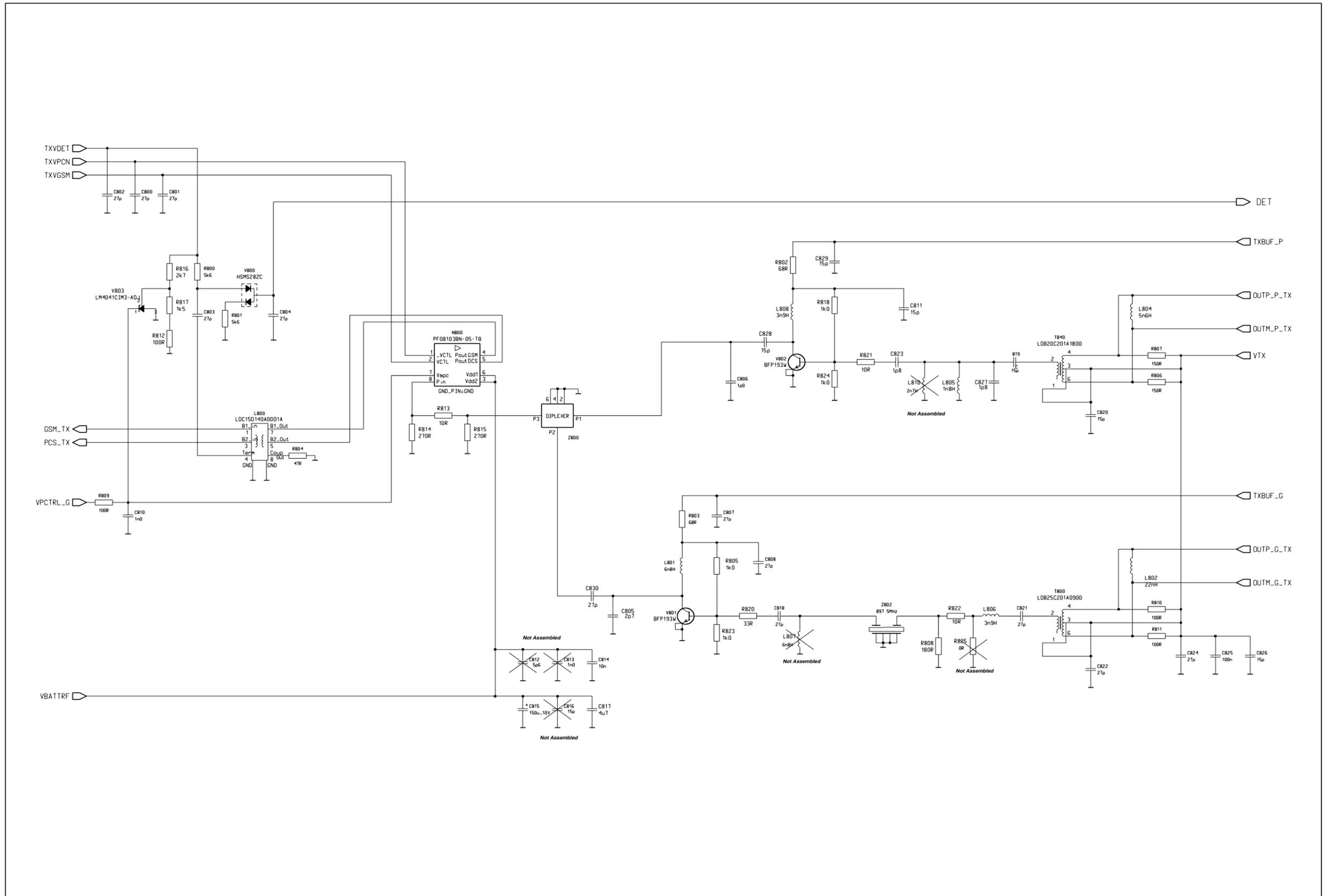


RX



PlotDate: Jun 22 14:21:39

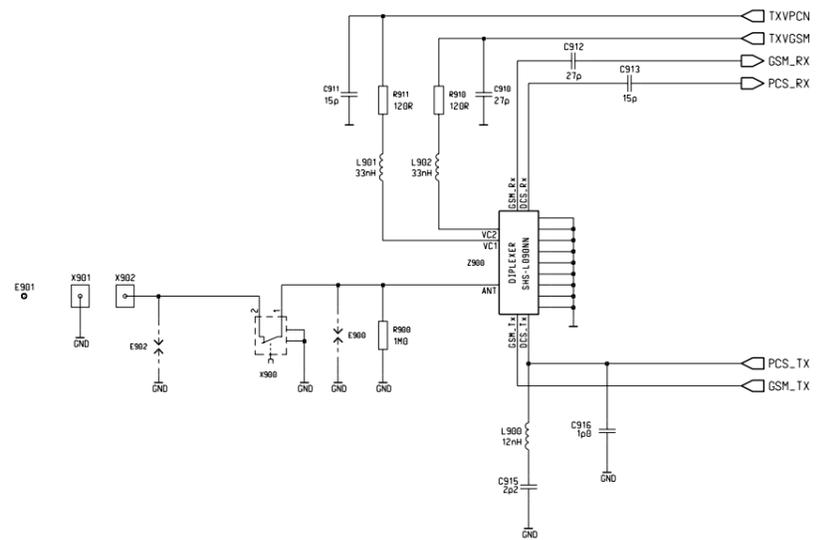
TX



Plot Date: Jun 22 14:21:39

Switch

Plot Date: Jun 22 14:21:39



Component Layouts

